

WEST Search History

DATE: Thursday, November 25, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L30	shared memor\$3 and L29	3
<input type="checkbox"/>	L29	matrix and diagonal and block\$1	146
<input type="checkbox"/>	L28	L27 and diagonal blocks	3
<input type="checkbox"/>	L27	lu or lower upper	1345185
<input type="checkbox"/>	L26	matrix blocks and diagonal blocks	0
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L25	(lu or lower upper) and L21	3
<input type="checkbox"/>	L24	(lu or lower upper) with L21	0
<input type="checkbox"/>	L23	L1 and L22	0
<input type="checkbox"/>	L22	L12 and L21	3
<input type="checkbox"/>	L21	(712/17).ccls.	72
<input type="checkbox"/>	L20	L12 and L19	0
<input type="checkbox"/>	L19	(708/514).ccls.	17
<input type="checkbox"/>	L18	L12 and L17	7
<input type="checkbox"/>	L17	(708/520).ccls.	148
<input type="checkbox"/>	L16	matrix blocks same diagonal blocks	4
<input type="checkbox"/>	L15	matrix blocks with diagonal blocks	2
<input type="checkbox"/>	L14	matrix with diagonal blocks	47
<input type="checkbox"/>	L13	matrix with diagonal with block\$1	396
<input type="checkbox"/>	L12	matrix same diagonal same block\$1	2058
<input type="checkbox"/>	L11	L1 and vector processor\$1	1
<input type="checkbox"/>	L10	L1 and array processor\$1	1
<input type="checkbox"/>	L9	L1 and L8	4
<input type="checkbox"/>	L8	parallel computer\$1	2529
<input type="checkbox"/>	L7	L1 and L6	5
<input type="checkbox"/>	L6	shared memor\$3	11499
<input type="checkbox"/>	L5	diagonal same L4	0
<input type="checkbox"/>	L4	lower upper factorization	3
<input type="checkbox"/>	L3	diagonal same L2	9
<input type="checkbox"/>	L2	matrix same L1	44
<input type="checkbox"/>	L1	lu factorization	60